Coupling between microstrip lines embedded in polyimide layers for 3D-MMICs on Si

G.E. Ponchak, E.M. Tentzeris and J. Papapolymerou

Abstract: Three-dimensional circuits built on multiple layers of polyimide are required for constructing Si/SiGe monolithic microwave/millimetre-wave integrated circuits on CMOS (low resistivity) Si wafers. However, the closely spaced transmission lines are susceptible to high levels of coupling, which degrade circuit performance. In this paper, finite difference time domain (FDTD) analysis and measured characteristics of novel shielding structures that significantly reduce coupling between embedded microstrip lines are presented. A discussion of the electric and magnetic field distributions for the coupled microstrip lines is presented to provide a physical rationale for the presented results.

1 Introduction

There is a rapidly expanding market for Si microwave/ millimetre-wave integrated circuits (MMICs) fabricated in standard CMOS foundries to replace GaAs MMICs in wireless communication systems, phased array radar, and other applications where the circuit cost is a major factor in determining the system cost. However, microwave passive elements and transmission lines placed directly on standard CMOS grade Si have low quality factors (high attenuation), which necessitates novel transmission line structures [1] that are typically embedded in polyimide that is deposited over the Si substrate. Moreover, highly integrated systems that include the RF circuits, digital data processing circuits, sensor circuits, and bias control circuits on a single chip or within a single package also rely on multiple layers of polyimide to construct three-dimensional circuits that are smaller than would normally be possible.

Although thin film microstrip (TFMS) embedded in polyimide solves the problem of high attenuation and smaller sized circuits, closely spaced transmission lines also increase the potential for high levels of coupling between lines. If the interline crosstalk is too high, the circuit characteristics are severely degraded. Thus, techniques and layout rules are required to reduce coupling between parallel TFMS lines. Prior papers on reducing coupling between microstrip lines built on low temperature cofired ceramic (LTCC) have shown that a roll of via holes placed between the two lines reduces coupling by 8 dB if the via holes are connected on the top and bottom by a strip and the ground plane, respectively [2, 3]. A continuous metal filled wall fabricated between two TFMS lines embedded within polyimide has been shown to also reduce coupling by approximately 8 dB for a single microstrip geometry [4], and preliminary work by the authors has shown that metal filled via post fences in polyimide provide the same level of coupling reduction [5].

In this paper, a systematic evaluation of the coupling between TFMS lines embedded in polyimide built on CMOS grade Si is presented for the first time. This in-depth characterisation includes coupling between microstrip lines on the same polyimide layer and between microstrip lines on different layers of polyimide layers. In addition, the use of metal filled via hole fences and metal walls embedded in the polyimide to reduce coupling are investigated. Finite difference time domain (FDTD) analysis and measurements are used to quantify the coupling, and FDTD generated electric and magnetic field plots are used to describe qualitatively the nature of the coupling.

2 Microstrip circuit description

Fig. 1 shows a cross-sectional cut through microstrip lines embedded in polyimide on a Si substrate. The microstrip ground plane covers the Si substrate, which shields all of the electromagnetic fields from the lossy Si. W1 and W2 are 23 µm and 52 µm respectively to yield a 50Ω transmission line for the polyimide thickness h of 10 μ m. Several shielding structures between the two microstrip lines are characterised. For microstrip lines on the same polyimide layer as shown in Fig. 1a, the shielding structures are: a row of metal filled via holes through polyimide layer 1 only; a row of metal filled via holes through both layers of polyimide; and a continuous, metal filled trench through both layers of polyimide. For microstrip lines on different layers of polyimide as shown in Fig. 1b, the shielding structures are a row of metal filled via holes through both layers of polyimide and a metal filled trench through both lavers of polyimide. In all cases with a row of metal filled via posts, the via holes are 20 by 20 µm and circuits are analysed with via hole spacing DV of 60 and 100 μm from centre to centre. Furthermore, all via holes are connected by a continuous. 20 µm wide metal strip on each layer as recommended in [2]. The metal filled trench shield is a 20 µm wide trench that is etched in the polyimide and filled with plated metal; the metal filled trench is as long as the coupling section.

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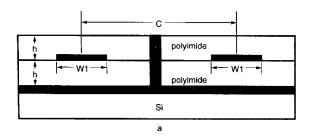
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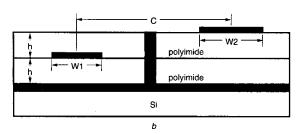


Fig. 1 Cross-sectional cuts through microstrip lines embedded in polyimide layers with a shielding structure between them a Microstrip lines on same layer of polyimide b Microstrip lines on different layers of polyimide

3 Theoretical analysis

The fullwave FDTD technique [6] is used for the theoretical characterisation of the forward and backward coupling, S_{31} and S_{41} respectively, between the two parallel microstrip lines (Fig. 2). The E- and H-field components are implemented in a leapfrog configuration. An adaptive grid with neighbouring cell aspect ratio smaller than or equal to 2 maintains a second-order global accuracy.

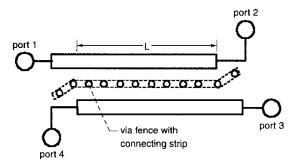


Fig. 2 Schematic of the coupled line structures used to characterise the coupling

Numerical meshes of 80 to 120 by 45 by 250 cells terminated with 10 perfectly matched layer (PML) cells in each direction provide accurate results for a time step of $\Delta t = 0.99 \Delta t_{\rm max}$. A Gaussian pulse with $f_{\rm max} = 60\,{\rm GHz}$ is applied vertically as a soft source close to the front end of the microstrip, and its values are superimposed on the FDTD calculated field value for all cells in the excitation region for each time step. The via holes are modelled as rectangular metal tubes with cross-section 23 by 20 μ m. To account for the coupling of even and odd modes, two simulations are performed for each geometry exciting both lines with equal amplitude and even or odd space distributions respectively. In addition, both microstrip lines are terminated with matched loads ($Z_{\rm o} = 50\,\Omega$) that are realised as the combination of shunt resistors placed

between the microstrip and the bottom ground [7]. Two probes placed at the front end and at the far end of one line are used for the combination of the results of the even and odd simulations. The application of the FFT algorithm derives the frequency-domain results from the time-domain data (usually 25,000 time steps).

4 Circuit fabrication and experimental procedure

The circuit for experimentally characterising coupling between the microstrip lines is shown in Fig. 2. The four-port circuit has probe pads orientated so that each port may be probed simultaneously. The coupling region, or the section of parallel transmission lines labelled L in Fig. 2, is 5000 µm long. Note that for characterisation, the shielding structure extends past the transmission line right angle bend and is tapered at 45° to minimise the effects of radiation from the right angle bend.

The four-port microstrip circuits are fabricated on a 1 Ω cm Si wafer. A ground plane consisting of a 300 Å Ti adhesion layer, 1.5 µm of Au, and a 200 Å Cr cap layer is first evaporated onto the Si wafer. This is followed by spinning on Dupont adhesion promoter and 10 µm of Dupont PI-2611 polyimide, which has a permittivity of 3.12 measured at 1 MHz [8] and a loss tangent of 0.002 measured at 1 kHz [9]. After curing the polyimide at 340°C for 120 min, Ni is evaporated onto the polyimide to serve as a mask for the O₂/CF₄ reactive ion etching (RIE) of the via holes. After the via holes are etched and the Ni removed, 200 Å of Ti and 2000 Å of Au are sputtered on to the wafer to serve as a seed layer for the 1.3 um of Au electroplating that is used to define the embedded microstrip lines and fill the via holes in a single step. These Au microstrip lines are capped with 200 Å of Cr before applying the next, 10 µm layer of polyimide. Thus, all metal structures are 1.5 µm thick. This process is repeated for each layer of polyimide. After each step, a DEKTAK surface profile is used to measure the polyimide and metal strip thickness. Both the DEKTAK and SEM analysis show that the surface roughness is low enough that it can be neglected in the analysis.

Measurements are made on a vector network analyser from 2 to 50 GHz. A thru/reflect/line (TRL) calibration is implemented with MULTICAL [10], a TRL software program, using four delay lines of 1800, 2400, 4800, and 10,000 μm and a short circuit reflect fabricated on the same substrate as the circuits. To improve accuracy, each circuit is measured several times and the average of these measurements is presented in this paper. During the measurement of the four-port circuits, two of the four ports are terminated in $50\,\Omega$ loads built into specially designed picoprobes.

5 Microstrip coupling results

As a first step, the measured and FDTD analysis results for the embedded microstrip lines are compared across the entire frequency band of 2 to 50 GHz. One such case for coupled microstrip lines without any shielding structure is shown in Fig. 3. It is seen that there is excellent agreement between the theory and the measured results, which is typical of the other cases. Also typical of all of the results presented in this paper, the forward coupling increases monotonically with frequency, while the backward coupling is periodic. Thus, throughout the paper, the backward coupling results presented are the maximum coupling value over the frequency band. Presented results are backward

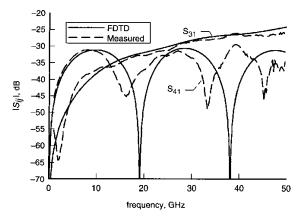


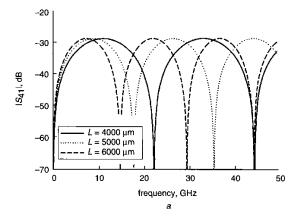
Fig. 3 Comparison of measured and FDTD scattering parameters for coupled microstrip lined on the same polyimide layer (Fig. 1a) with no shielding structure and $C=69\,\mu m$

coupling defined as $-20 \log |S_{41}|$ and forward coupling defined as $-20 \log |S_{31}|$. Note that the definition for forward coupling presented here differs from the definition typically used in the literature which is S_{31}/S_{21} [11], but if the coupling and attenuation for the lines is very small, $|S_{21}|$ is approximately equal to one and the two definitions are equivalent. Also, backward coupling is often called near end coupling and forward coupling is often called far end coupling in the literature [11].

While all of the results in this paper are presented for two coupled lines with a coupling length of $5000\,\mu m$, an FDTD analysis of microstrip lines with shielding structures as a function of coupling length was performed. Fig. 4 shows that the S-parameters for two microstrip lines separated by $C = 72 \,\mu\text{m}$ and a continuous metal filled trench. As seen in Fig. 4a, the maximum level of backward coupling is independent of the coupling length, and only the periodicity of the nulls changes. The magnitude of the forward coupling for shielded microstrip lines with different coupling lengths varies with frequency in a similar manner as shown in Fig. 3, which is for unshielded coupled microstrip lines, and to a first order, the forward coupling increases linearly with coupling length. These results agree with the general conclusions for coupling between weakly coupled transmission lines [11], which show that the shielding structures do not change the basic physics that cause coupling.

The effect of shielding structures on the coupling level between parallel microstrip lines fabricated on the same layer, as shown in Fig. 1a, is summarised in Fig. 5. For closely spaced microstrip lines without shielding structures, the backward coupling is slightly stronger than the forward coupling, but for widely spaced lines, the forward coupling is approximately 5dB stronger. It is also seen that for closely spaced lines without shielding structures, the coupling is very high (approximately 30dB), but the coupling decreases monotonically to approximately 45dB as C increases.

When a shielding structure is introduced in the first layer of polyimide only (shield height equal to the microstrip height, h), the backward coupling is reduced by 8 dB for closely spaced lines, but there is very little improvement for larger line spacing or in the forward coupling. However, if the via fence or a continuous trench is placed through both layers of polyimide (shield height equal to 2h), the backward coupling is reduced by approximately 18 dB and the forward coupling is reduced by approximately 12 dB at 25 GHz for closely spaced lines. Moreover, it is



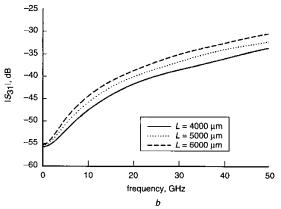
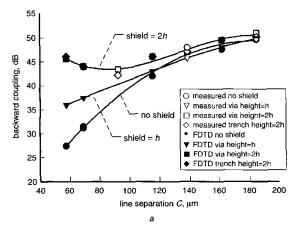


Fig. 4 FDTD derived S-parameters as a function of coupling length and frequency for coupled microstrip lines on the same polyimide layer (Fig. 1a) with a trench shielding structure and $C=72\,\mu m$

seen that the use of shielding structures enables microstrip transmission lines to be placed as close as $60\,\mu m$ while yielding the same coupling as lines with no shielding structures placed $130\,\mu m$ apart.

It is interesting that the via fence interconnected by a metal strip yields the same coupling level as the metal filled trench. Also, although not shown, it was found that the results are not dependent on the via hole spacing (DV = 60or 100 μm). However, these results are expected when the dimensions of the rectangular mesh created by the vias and metal strips that interconnect them is compared to a wavelength. Using the largest dimensions that were characterised, the mesh is 10 by 80 µm, which at 50 GHz results in the mesh being 0.003 by 0.023 λ_d where λ_d is the wavelength in the dielectric. Thus, the via fence appears electrically to be a solid wall. Another interesting result shown in Fig. 5 is that the coupling between microstrip lines with shielding structures with a height of 2h is very low for small C, it then increases as C increases, and finally reduces again as C increases further. The small C result is believed to be true, but it is not very useful in practice because the microstrip line is nearly touching the shielding structure for small C and the microstrip fields are greatly perturbed. In fact, placing the shielding structure too close to the microstrip line results in an asymmetric surface current on the microstrip, resulting in a higher conductor loss.

It is seen in Fig. 6 that for all frequencies, coupling decreases as the line separation increases with the same slope. Furthermore, for any line separation, the shielding structure reduces forward coupling by the same amount



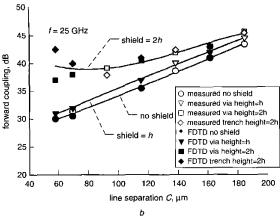


Fig. 5 Measured and FDTD backward coupling (a) and forward coupling (b) at 25 GHz of microstrip lines fabricated on the same layer of polyimide (Fig. 1a) as a function of centre-to-centre spacing,

across the entire frequency band. Lastly, as seen in Figs. 3 and 6, forward coupling is very low at low frequencies, but increases rapidly with frequency. Therefore, at low frequencies, backward coupling dominates, but for frequencies greater than 25 GHz, forward coupling dominates. Also, Fig. 6 shows that the results shown in Fig. 5b, and the conclusions drawn from them, can be applied to the entire microwave frequency spectrum.

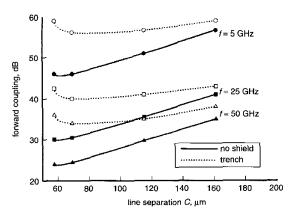
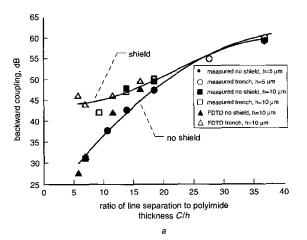


Fig. 6 Forward coupling of microstrip lines fabricated on the same layer of polyimide (Fig. 1a) as a function of frequency

Coupling between unshielded microstrip lines is typically assumed to be dependent on the line separation C, normalised by the substrate thickness h, or coupling is dependent on C/h [12, 13]. To investigate the effect of shielding structures on this relationship, a set of coupled microstrip lines were fabricated and characterised as described previously, except $h = 5 \,\mu\text{m}$ and $W1 = 12 \,\mu\text{m}$. The via hole dimensions and trench were kept at the previously described dimensions. This yields a $50\,\Omega$ microstrip line that is an exact scaled version of the previously reported results. Fig. 7 shows the measured and FDTD determined backward and forward coupling for both sets of circuits (h = 10 and $5 \mu m$). It is seen that the results when there are no via holes (filled symbols) supports the assumption that coupling is dependent on C/h. Furthermore, the results also show that this relationship holds when shielding structures are used to minimise coupling.



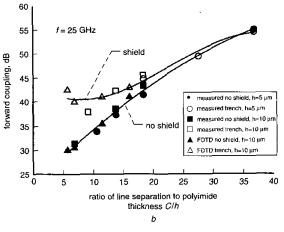


Fig. 7 Backward and forward coupling between microstrip lines as shown in Fig. 1a with $h=10~\mu m$ and $h=5~\mu m$ All lines are $50~\Omega$

The measured coupling between microstrip lines fabricated on different layers of polyimide as shown in Fig. 1b are summarized in Fig. 8. Generally, the observations pertaining to Fig. 5 are true here as well, but the reduction in coupling with shielding structures is very small, approximately 5 dB, which is the approximate reduction in coupling when the shielding structure height is equal to the substrate height h, in the previous results. Comparing

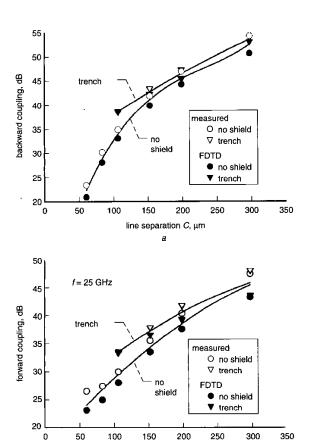


Fig. 8 Measured backward coupling (a) and forward coupling (b) at 25 GHz of microstrip lines fabricated on different layers of polyimide (Fig. 1b) as a function of centre-to-centre spacing C

line separation C, μm

the two different cases shown in Figs. 1a and 1b, it is seen that microstrip lines embedded on the same layer of polyimide with shielding structures that extend to the top of polyimide have approximately 10 dB less coupling than microstrip lines on different layers with the same shielding structure. Thus, while it may be necessary to place microstrip transmission lines on different layers of polyimide to reduce circuit size, this will result in higher coupling. It has also been shown that these results scale with the polyimide thickness, which enables designers to use these results for all 50 Ω microstrip lines embedded in polyimide.

6 Physics of coupling between microstrip lines

Up to this point, it has been shown that shielding structures placed between adjacent microstrip lines can reduce the coupling between them, but the nature of the coupling and how the shielding structures work has not been explored. The FDTD analysis is useful for this discussion since it can generate electric and magnetic field plots, which provide valuable insights.

Fig. 9 shows the electric and magnetic field plots of two microstrip lines on the same layer of polyimide (Fig. 1a) with no shielding structure. The line on the left is the excited line, while the line on the right is the coupled line. Comparing the electric (Fig. 9a) and magnetic (Fig. 9b) field distributions, it is seen that the coupling is primarily due to the magnetic field. In fact, the maximum field

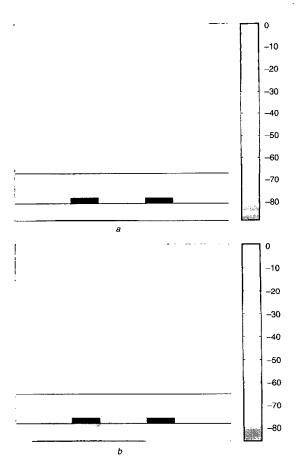


Fig. 9 Electric field (a) and magnetic field (b) distribution for microstrip lines ($C=69\,\mu\mathrm{m}$) on the same polyimide layer without shielding structure (Fig. 1a)

magnitude under the centre of the coupled line is -34.28 dB for the electric field and $-32.83\,\mathrm{dB}$ for the magnetic field. Fig. 10 shows the electric and magnetic field distributions for the same microstrip lines when a shielding structure extends to the top of the polyimide. Examining the electric field distribution in Fig. 10a first, it is seen that the shield is very effective and reduces the maximum electric field under the centre of the coupled line to $-45.72 \, \mathrm{dB}$. However, the magnetic fields shown in Fig. 10b are able to encircle the coupled microstrip line and still excite the microstrip mode. The maximum magnetic field under the centre of the coupled line is now -41.57 dB. When the coupled microstrip lines are on different layers of polyimide, similar results are observed with the shield reducing the electric field by 7.3 dB and the magnetic field by 5.1 dB. Thus, shielding structures are effective in reducing the electric field coupling, but the magnetic field coupling remains the dominant coupling mechanism and it is only moderately reduced by shielding

To derive general conclusions about the effectiveness of shielding structures in reducing electric and magnetic fields, the FDTD analysis was used to determine the excited fields at different points with and without a shielding structure. The analysis was first completed as a function of frequency, but it is found that the ratio of fields shielded to unshielded is not frequency dependent, which confirms the conclusions drawn from Fig. 6. Fig. 11a shows a schematic of the single microstrip line that was analysed with and without the shielding structure, which was a trench, and Fig. 11b shows the ratio of shielded to unshielded electric and magnetic

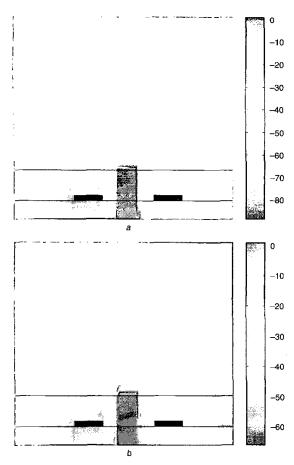
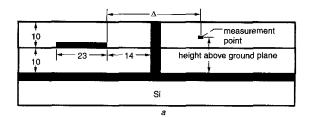


Fig. 10 Electric field (a) and magnetic field (b) distribution for microstrip lines ($C=69 \mu m$) on the same polyimide layer with shielding structure

fields as a function of position. It is seen that for small horizontal distance Δ , the electric field is reduced more than the magnetic field, but for large Δ , the magnetic field is reduced slightly more than the electric field. It is also seen that the field reduction is greatest close to the ground plane, and the field reduction decreases as the surface of the polyimide is approached. This confirms the coupling results of Figs. 5 and 8 for coupled microstrip lines at 10 and 20 μm respectively.

Lastly, it may be asked if coupling between microstrip lines is due to electromagnetic fields that leak through the via fence or if it is a result of electromagnetic fields that extend over the top of the wall. Fig. 10 appears to indicate that the fields extend over the top of the wall, it was already stated that the mesh created by the via fence is electrically small and therefore appears as a continuous wall, and that the measured coupling for the via fence and a trench wall are the same. These all indicate that coupling is not due to leakage through the wall. To confirm this, a FDTD analysis was completed for a structure similar to that shown in Fig. 11a, but in this case, the via fence was made to extend to a height of 100 µm, or ten times the microstrip substrate height. The level of the electromagnetic fields on the other side of the shield from the microstrip are determined to be at least 76 dB below the fields under the microstrip line. Since this is close to the numerical error of $-85 \, \mathrm{dB}$, it is concluded that via fence shields interconnected by metal strips prevent electromagnetic field leakage, and coupling is due to fields that extend over the top of the shield.



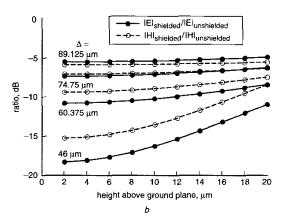


Fig. 11 Schematic of FDTD analysed structure (a), and reduction in electric and magnetic field due to a shielding structure as a function of position (b)

7 Conclusions

In this paper, a systematic evaluation of shielding structures for reducing coupling between microstrip lines embedded in polyimide has been presented. The results show that coupling is lower when both microstrip lines are on the same layer of polyimide and when the shielding structure extends through both layers of polyimide, or the shielding structures are more effective when they are higher than the microstrip substrate height. A shield comprised of metal posts interconnected by metal strips on the top and immediate layers provides the same shielding level as a continuous metal wall, at least to the measurement and numerical errors. The shielding structures are effective across the microwave spectrum, and it was shown that the results presented scale so that they are applicable to thinner or thicker polyimide circuits. Finally, it was shown that coupling between microstrip lines is primarily due to magnetic fields, and the shielding structures are effective in reducing both electric and magnetic field coupling.

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